

Fig. 1

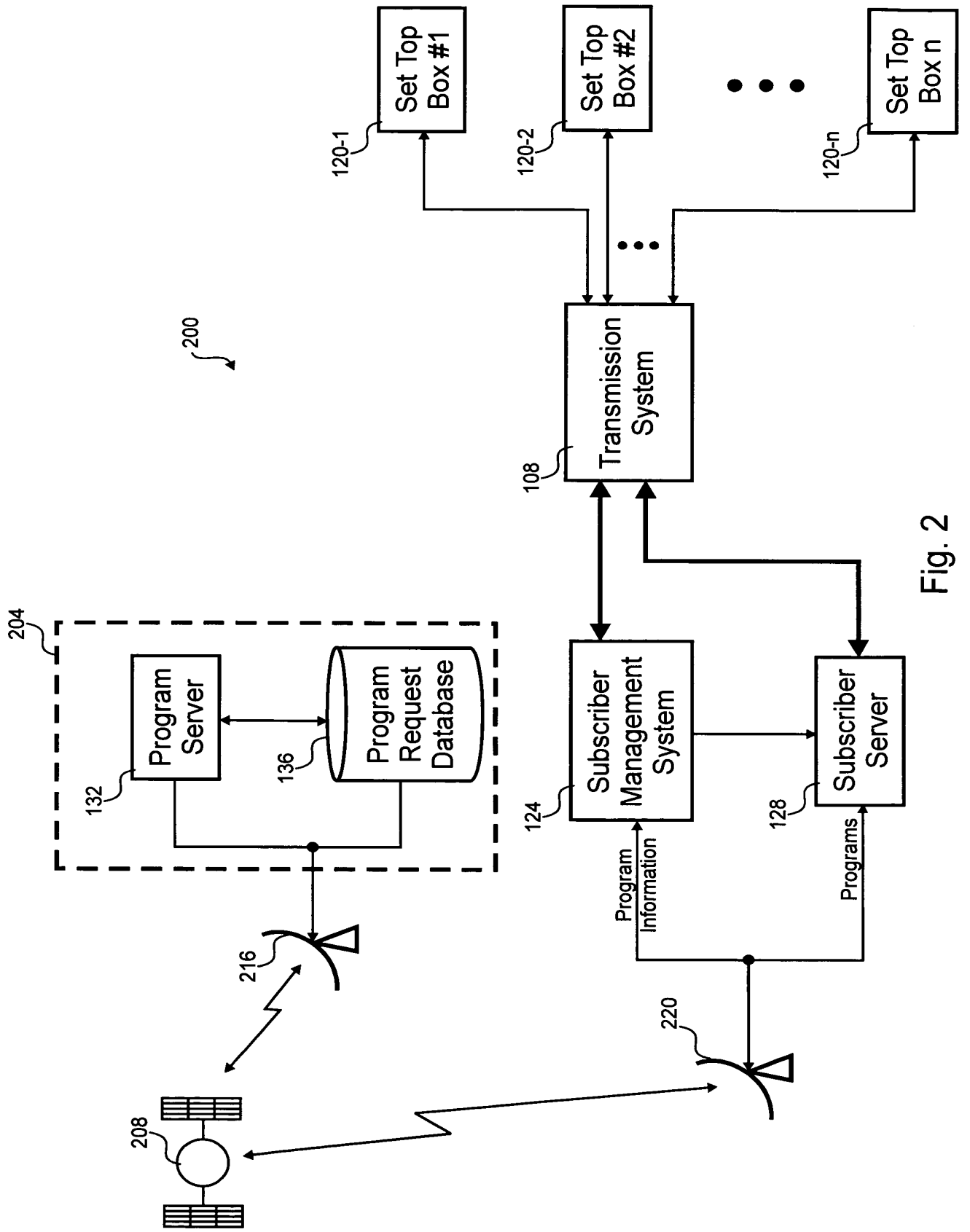
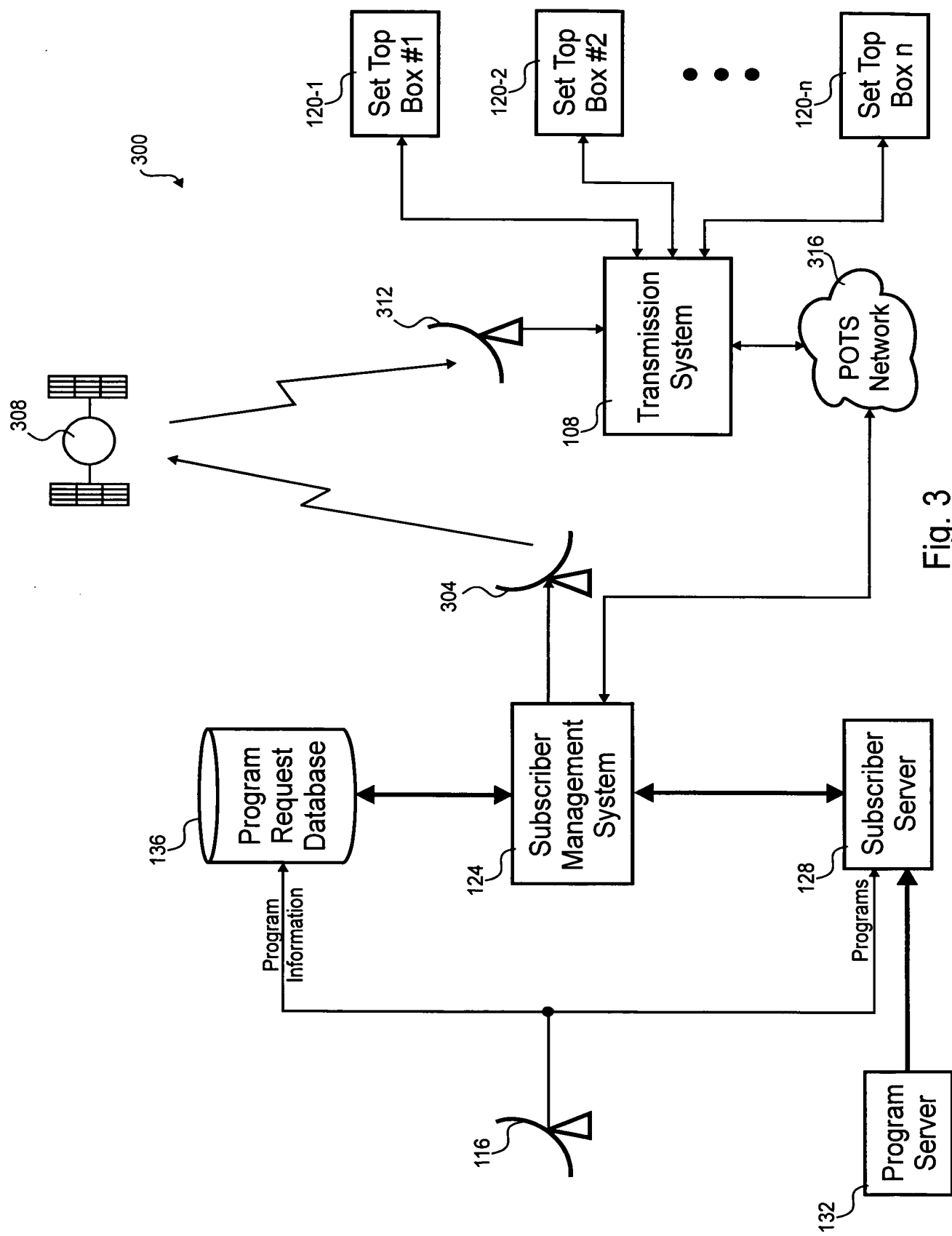


Fig. 2



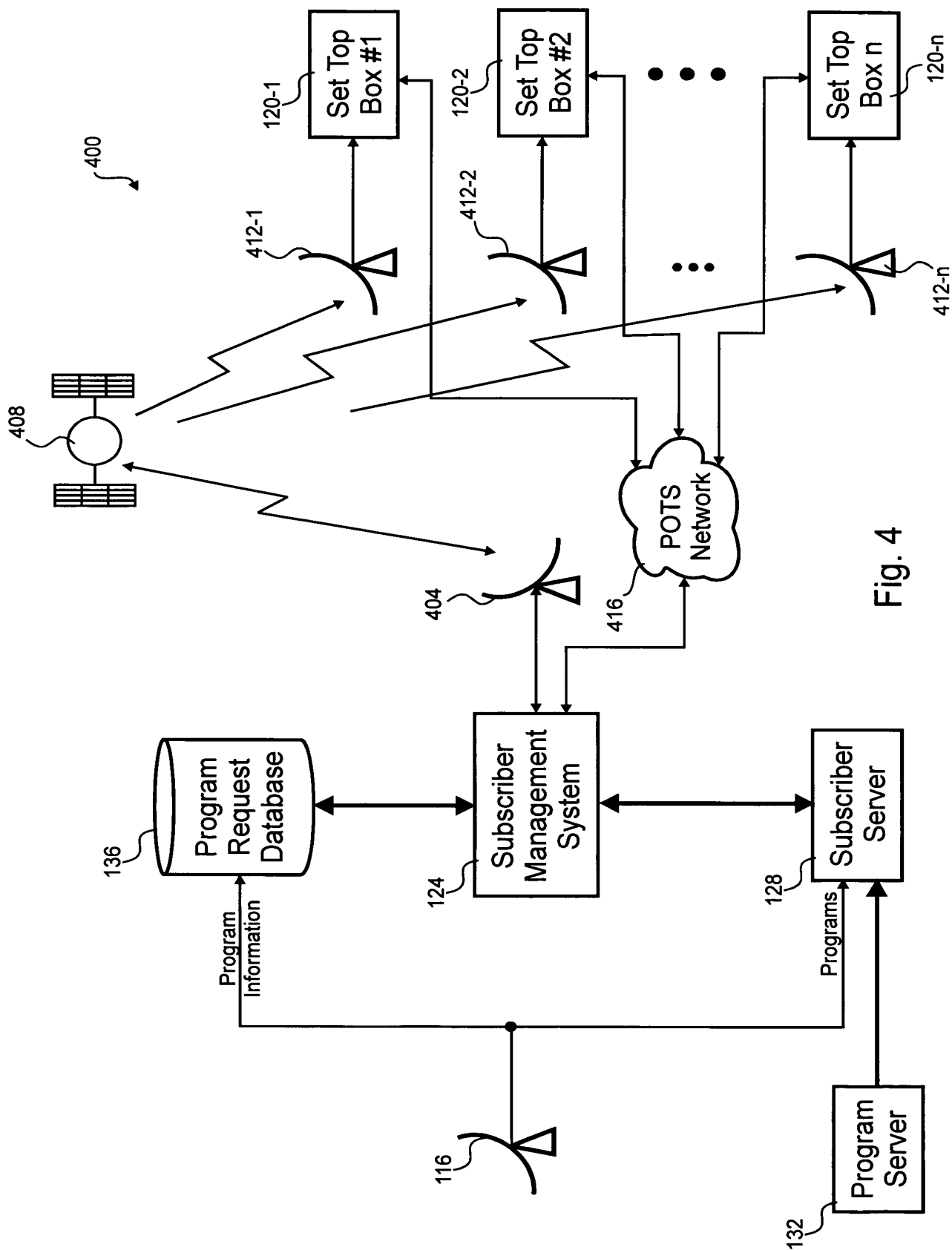


Fig. 4

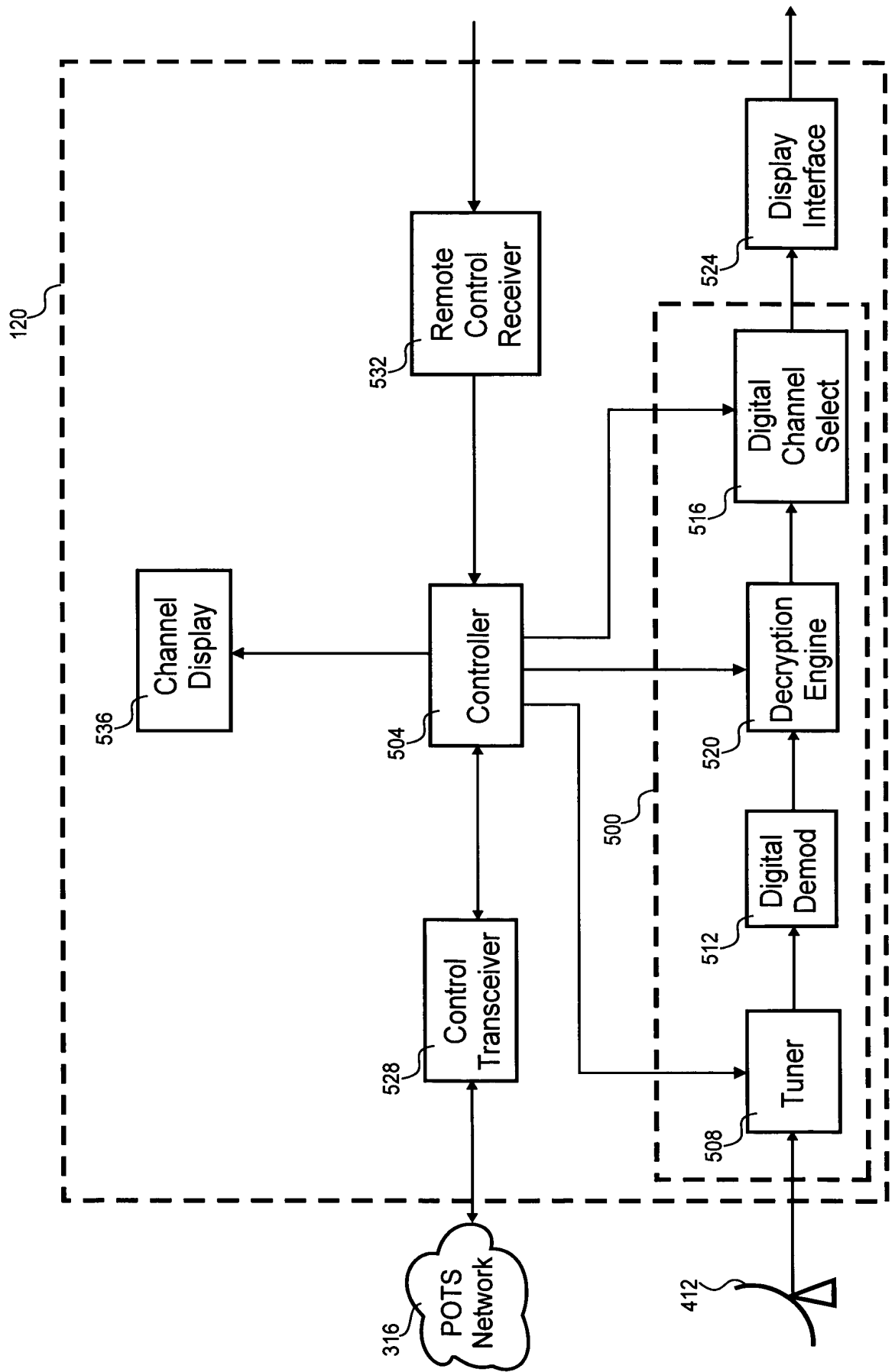


Fig. 5

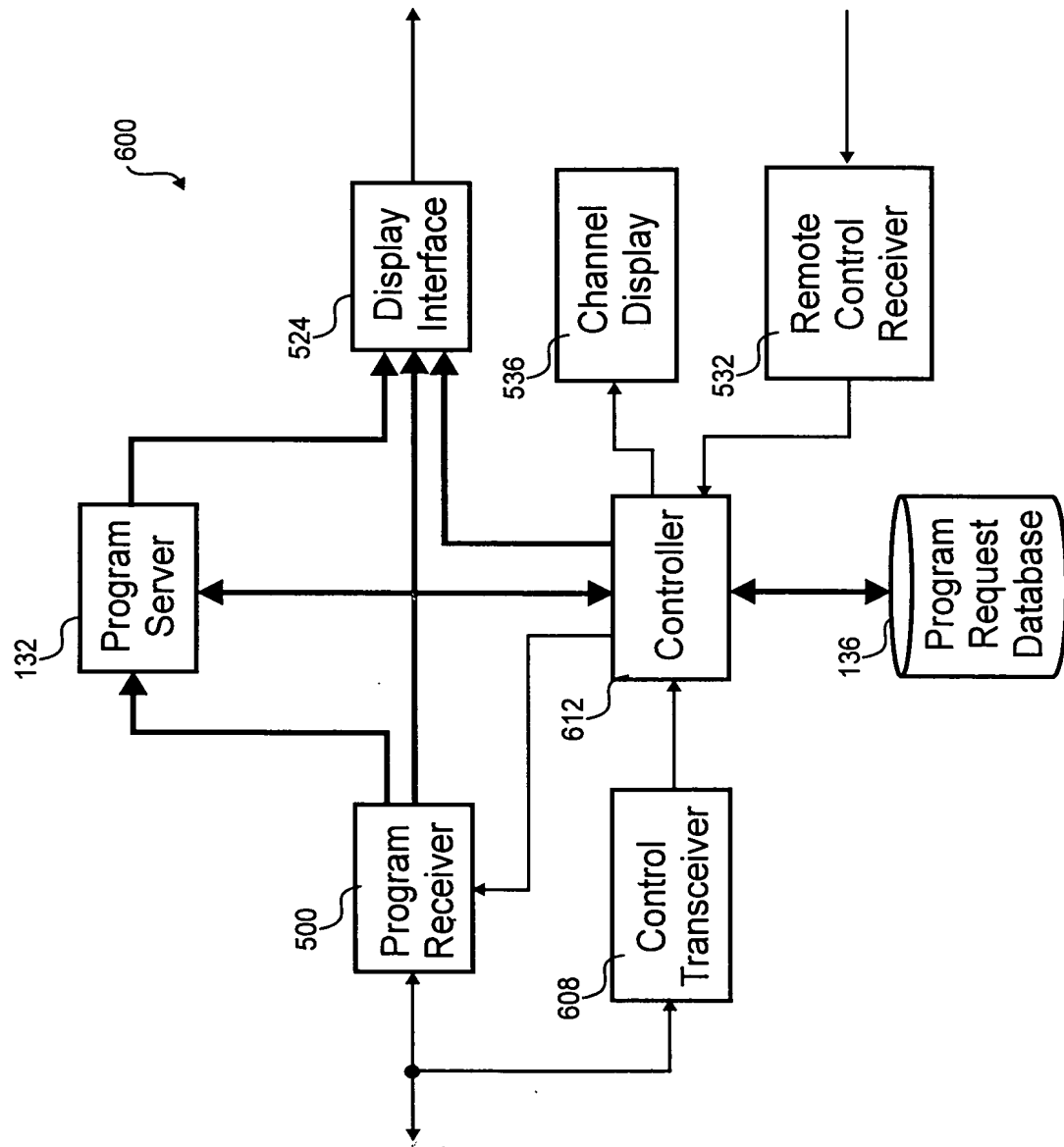


Fig. 6

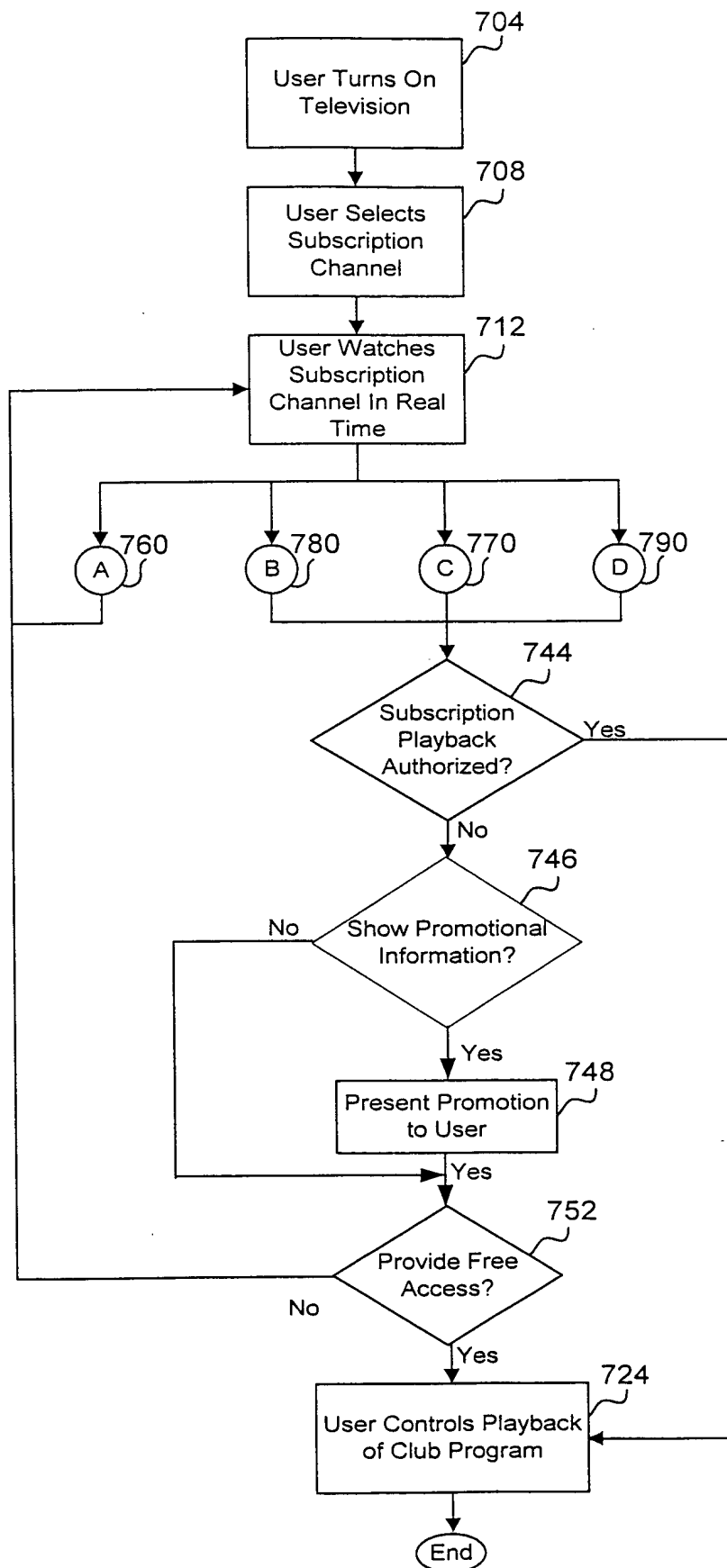


FIG. 7

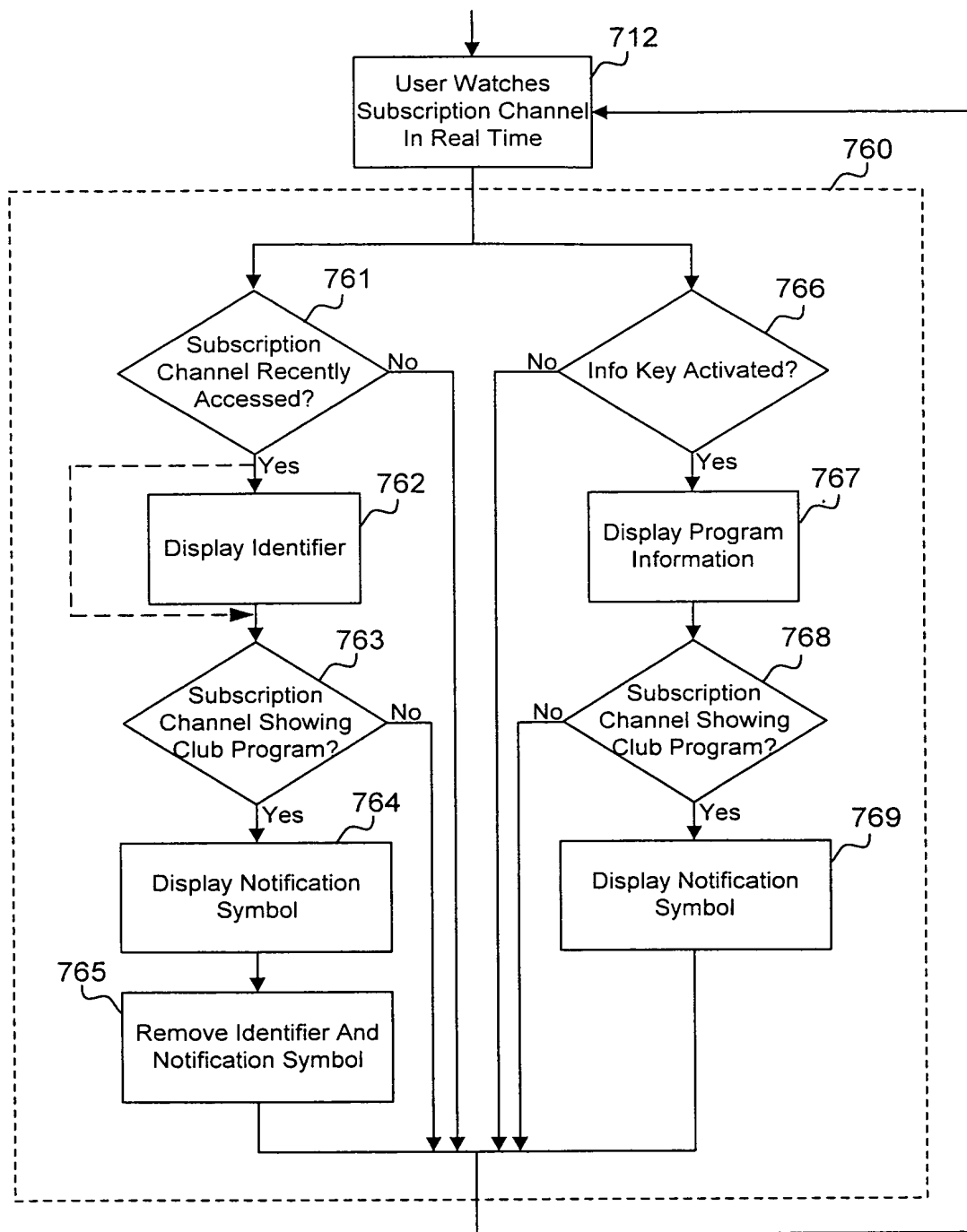


FIG. 7A

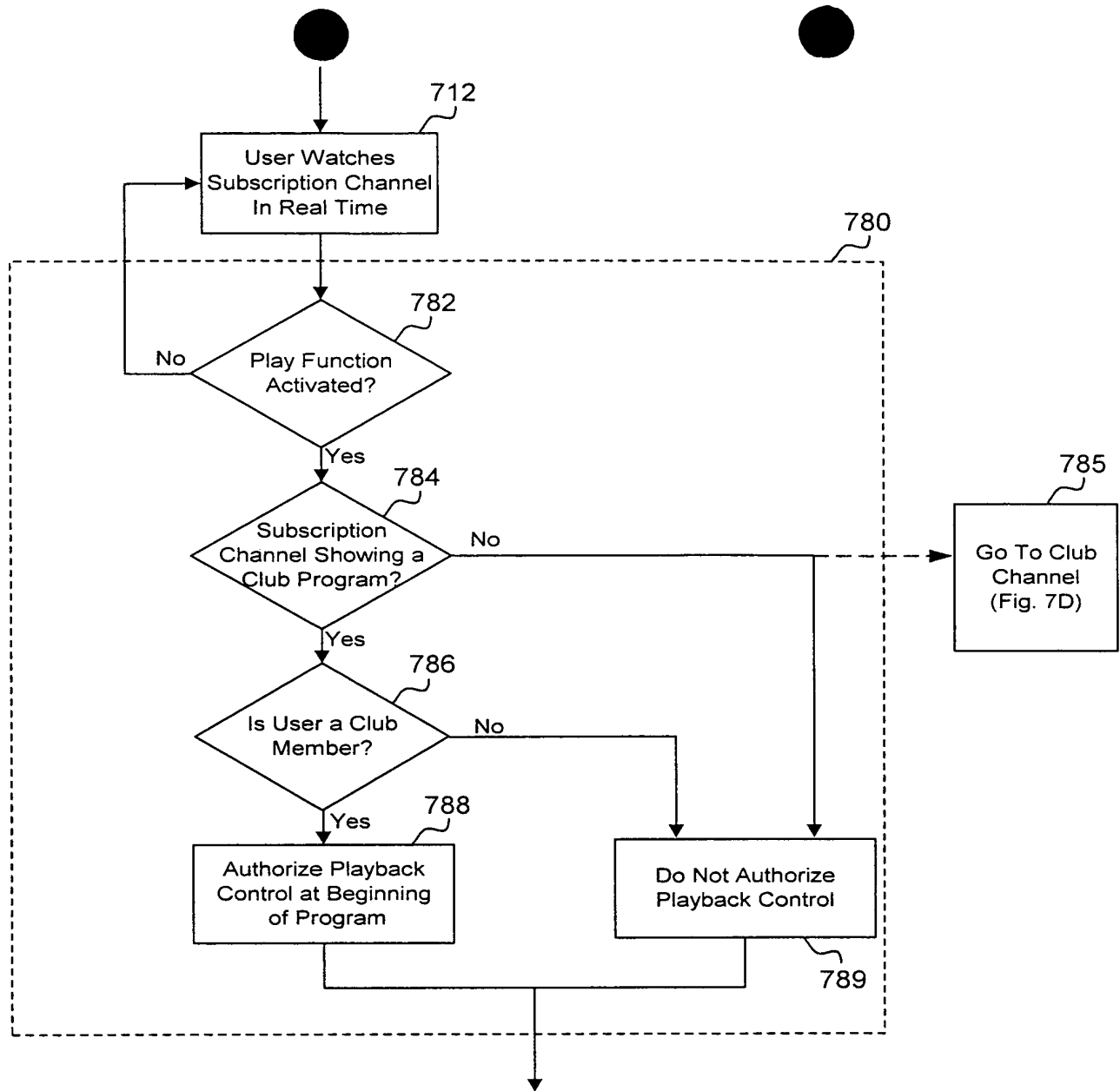


FIG. 7B

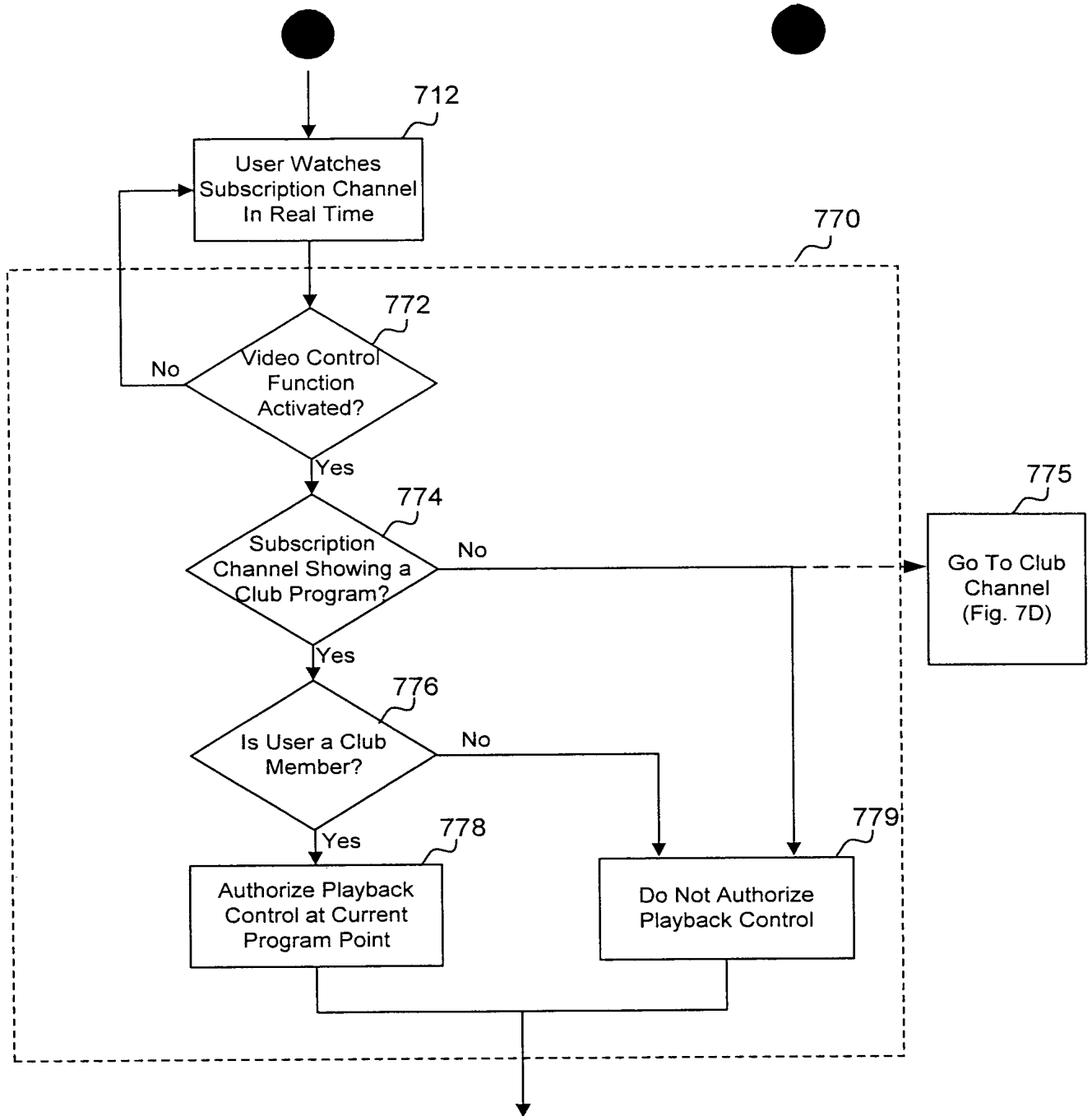


FIG. 7C

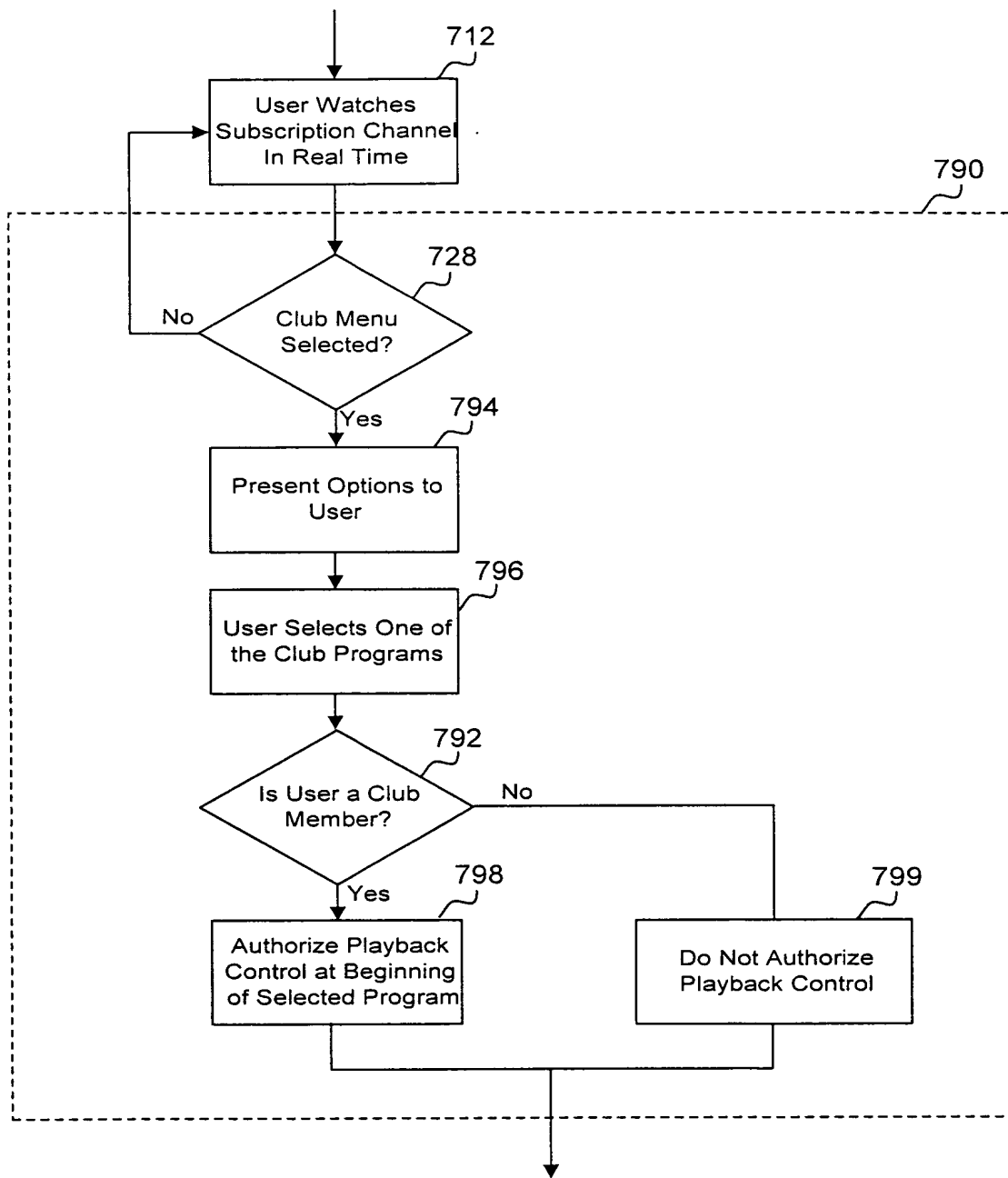


FIG. 7D

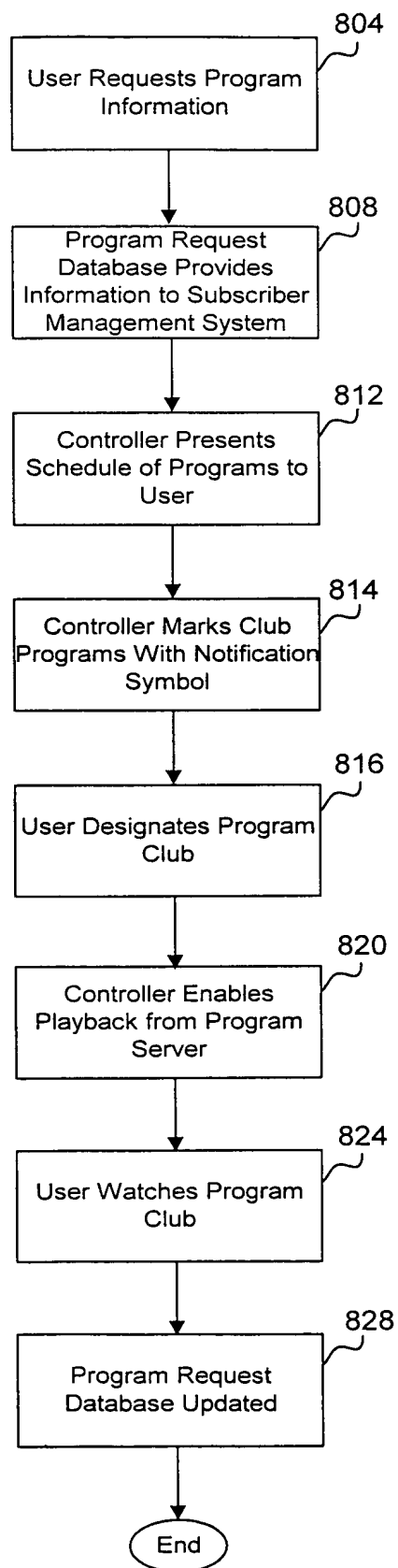


FIG. 8A

FIG. 8B

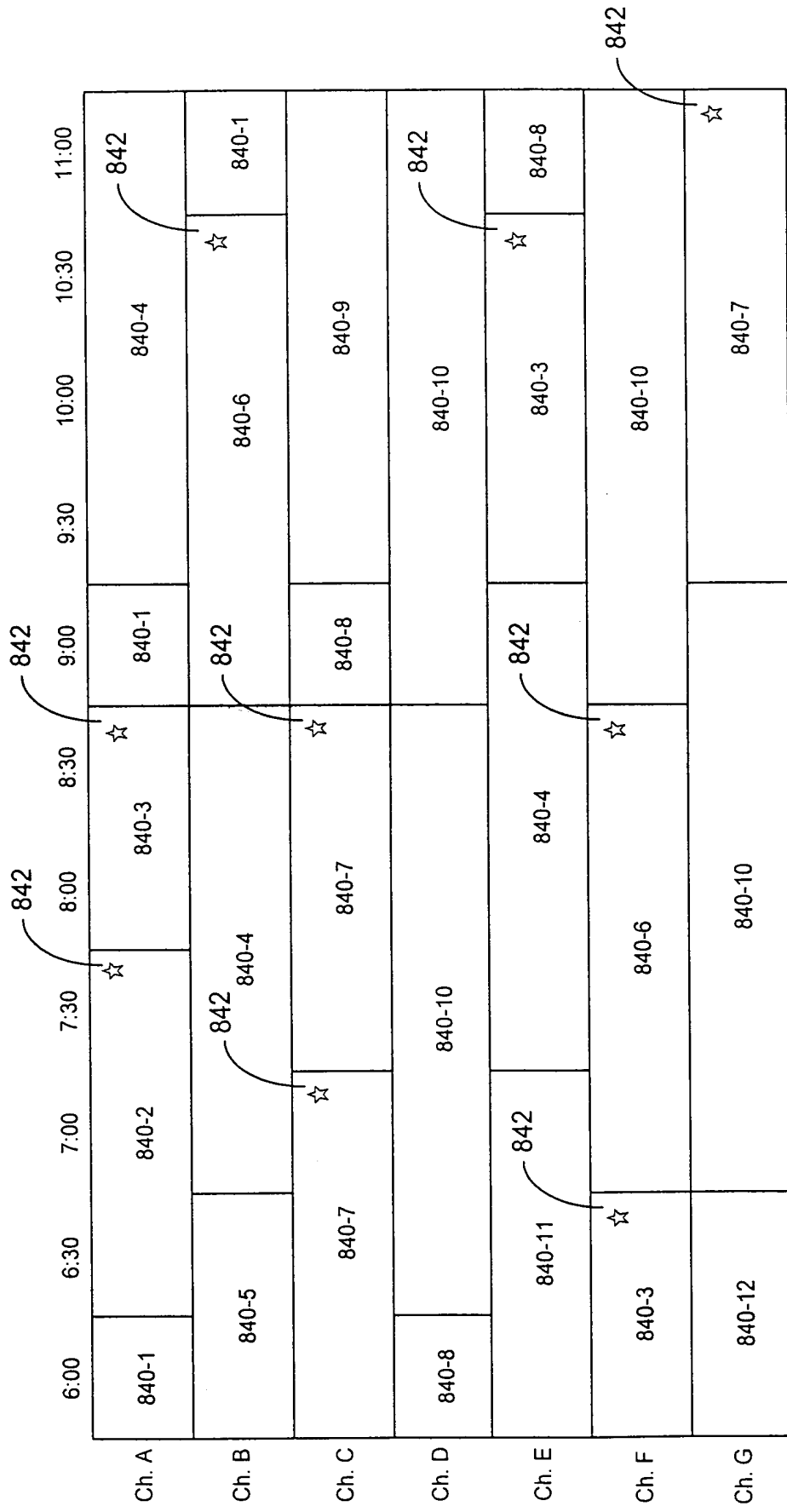
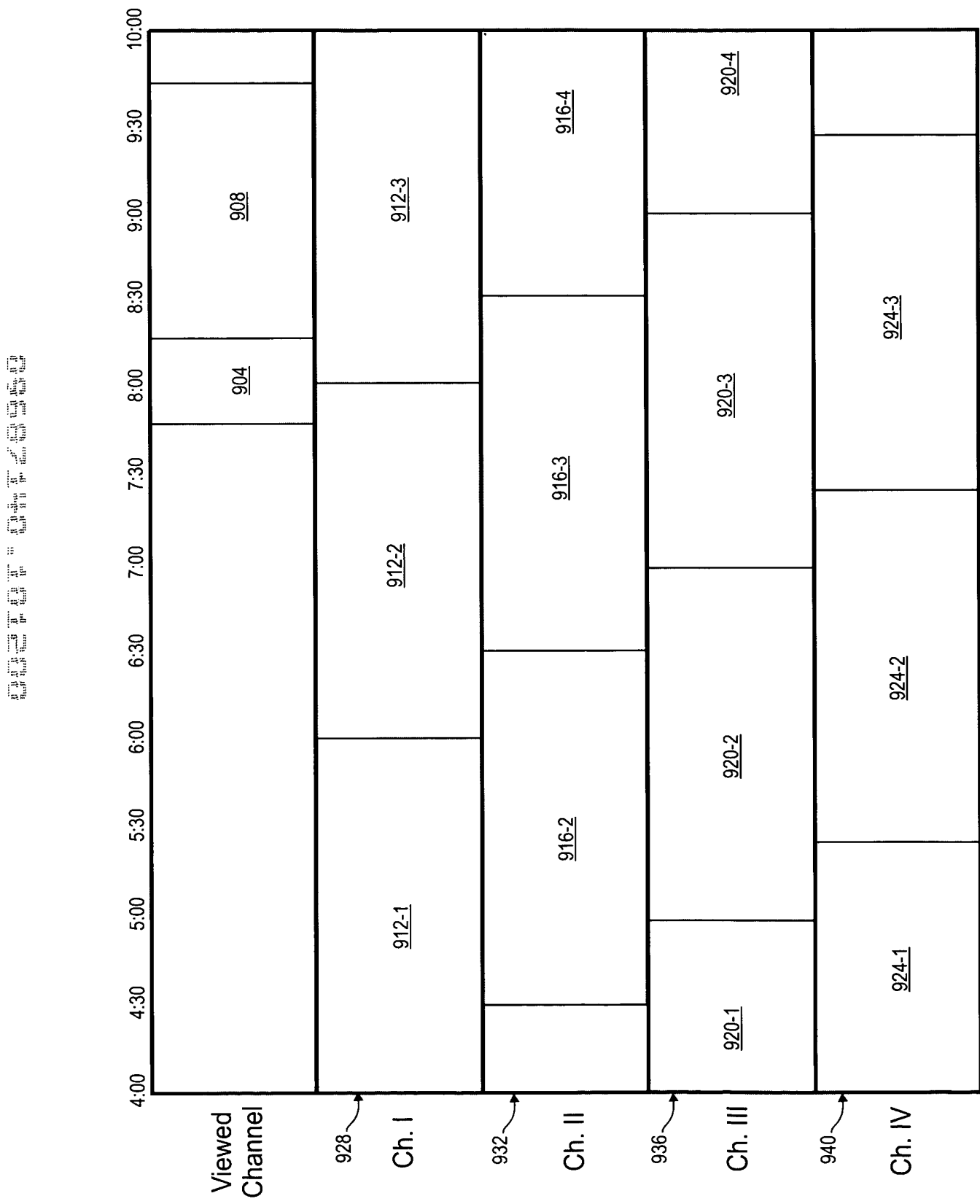


FIG. 8B



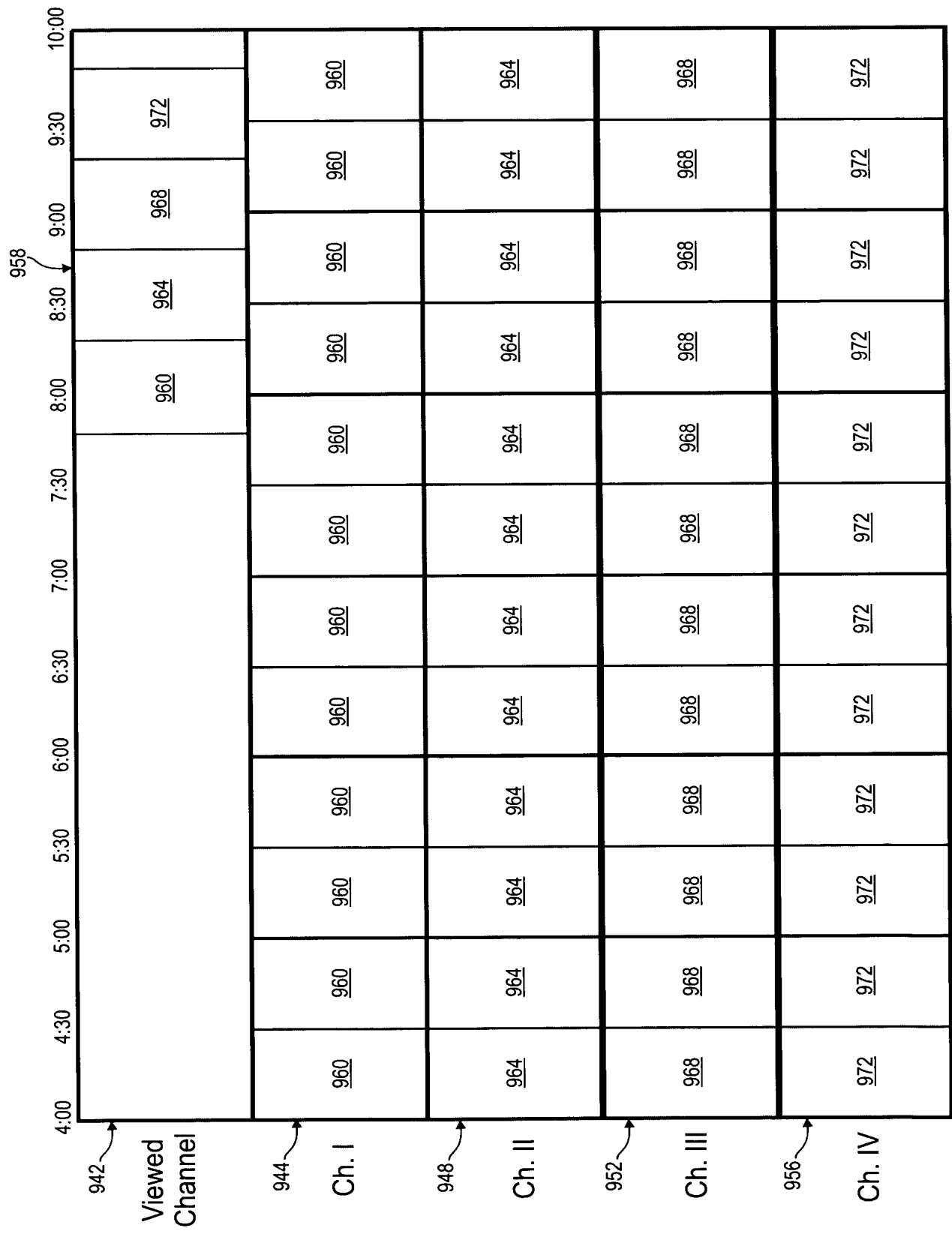


Fig. 9B

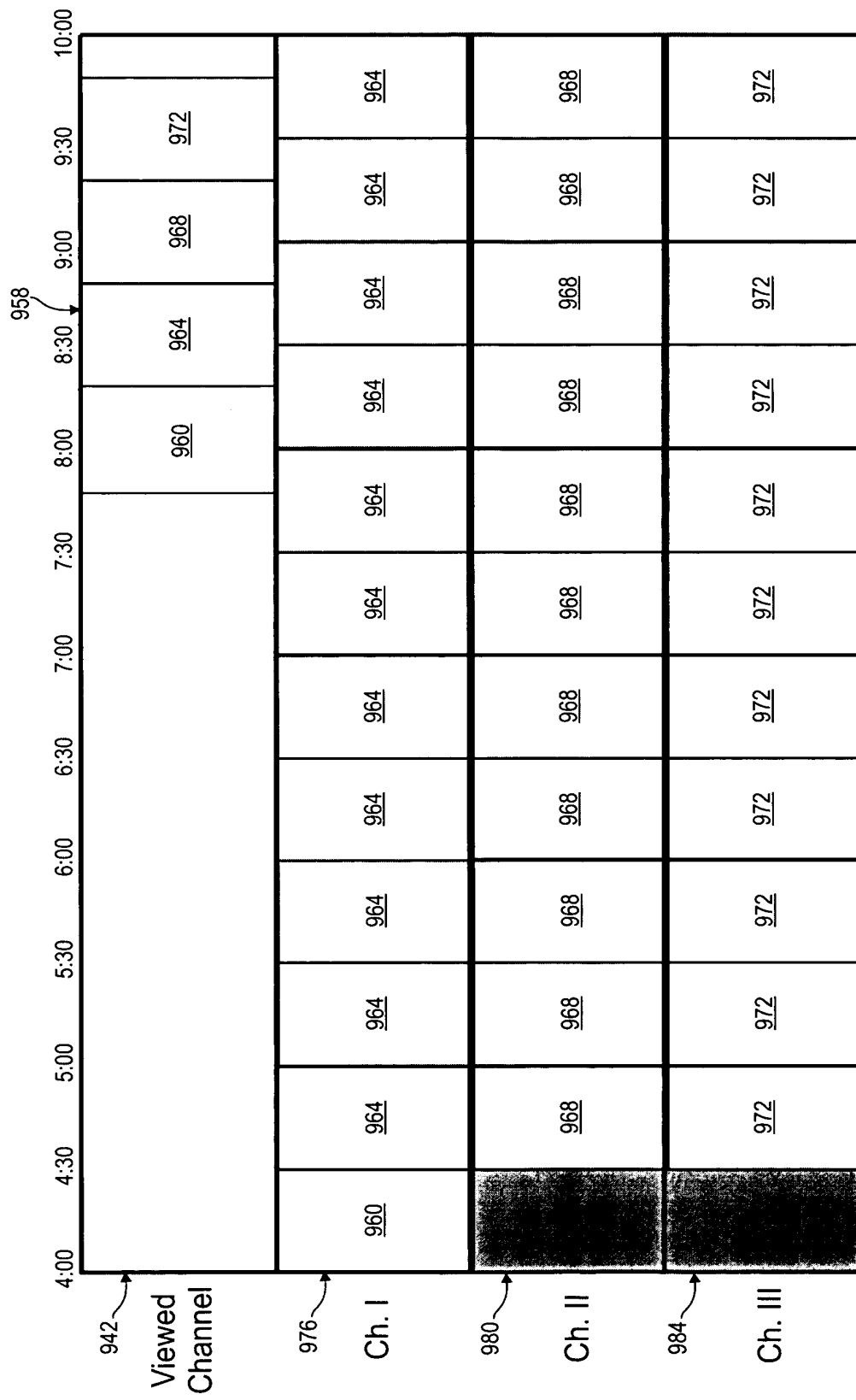


Fig. 9C

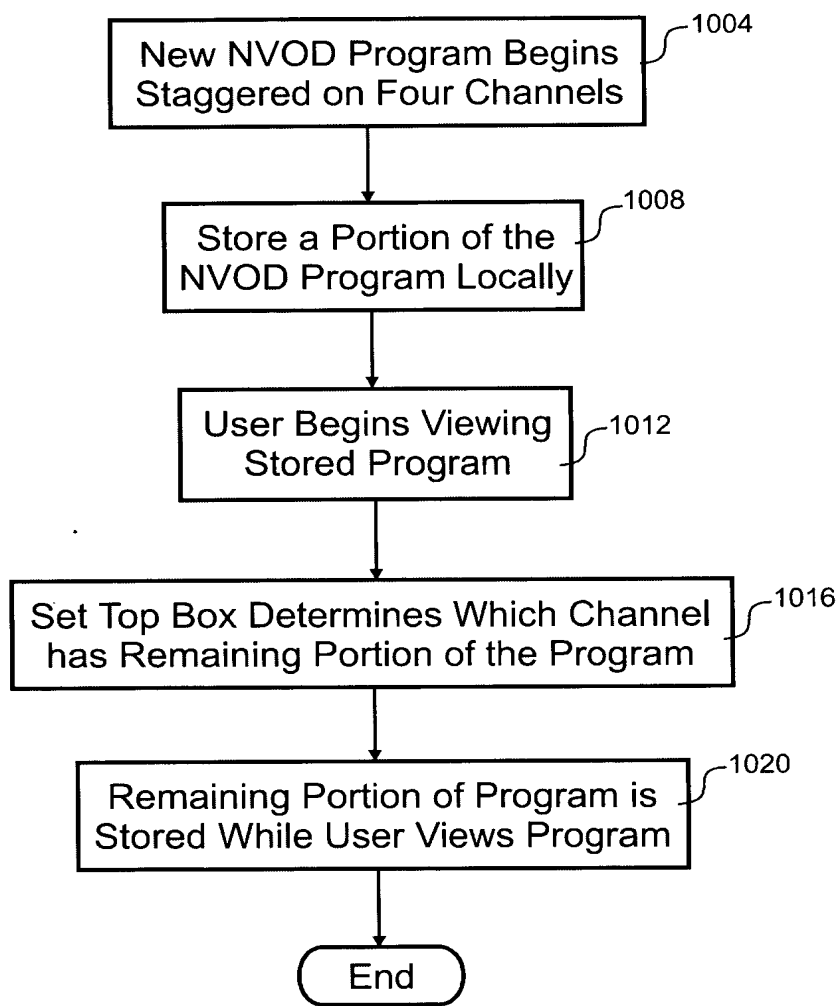


Fig. 10A

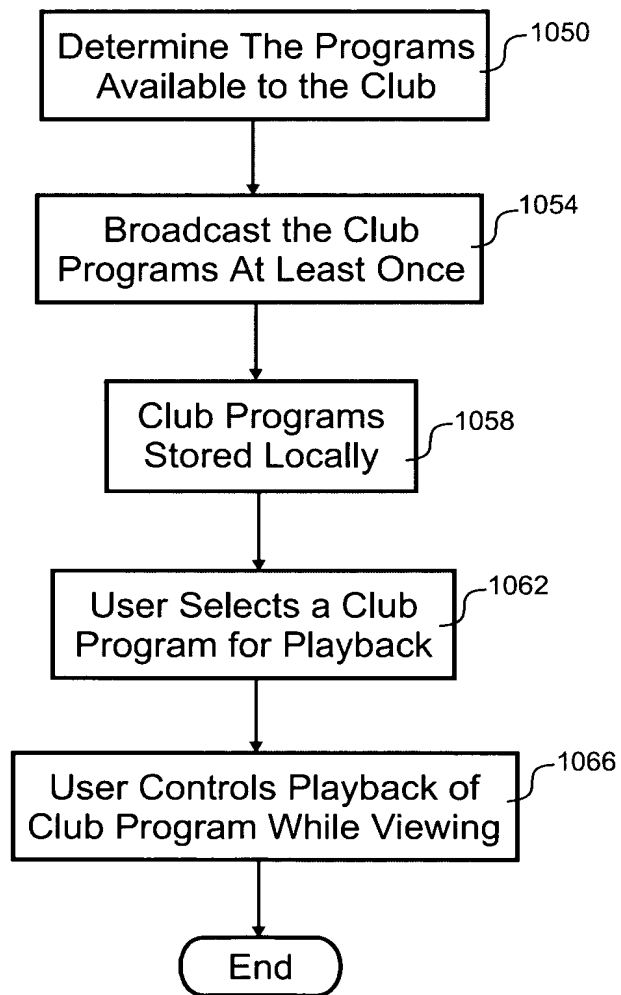


Fig. 10B

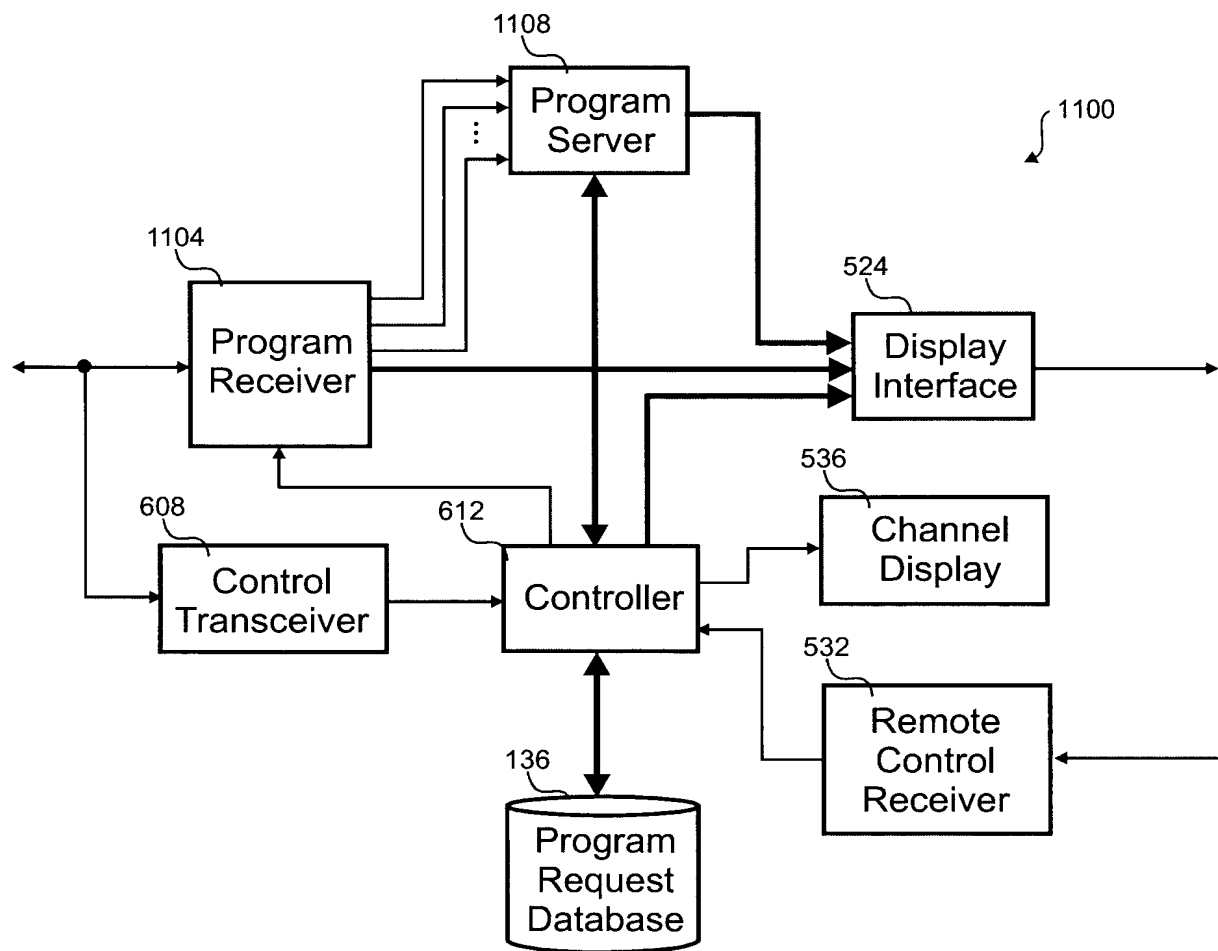


Fig. 11

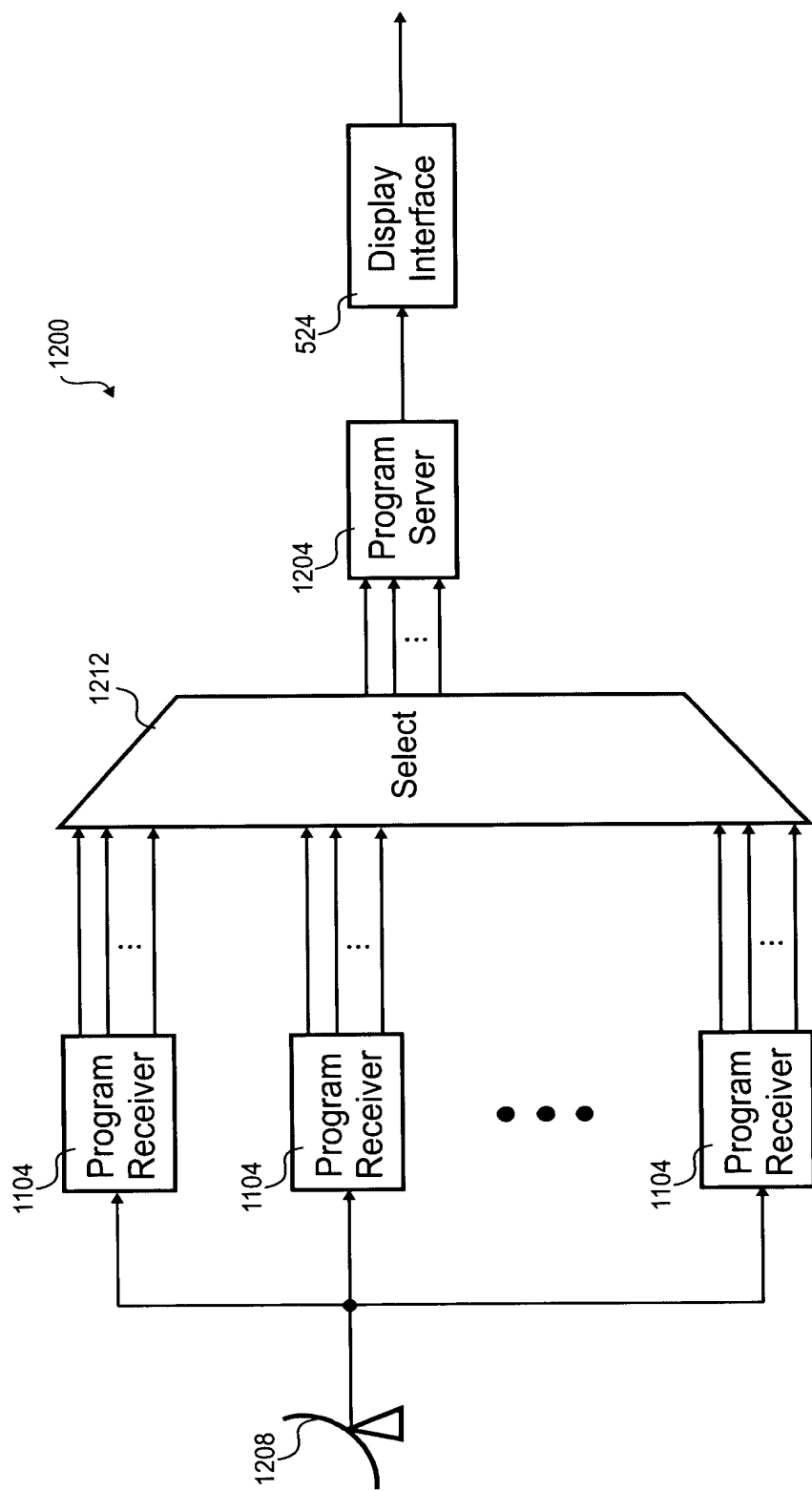


Fig. 12A

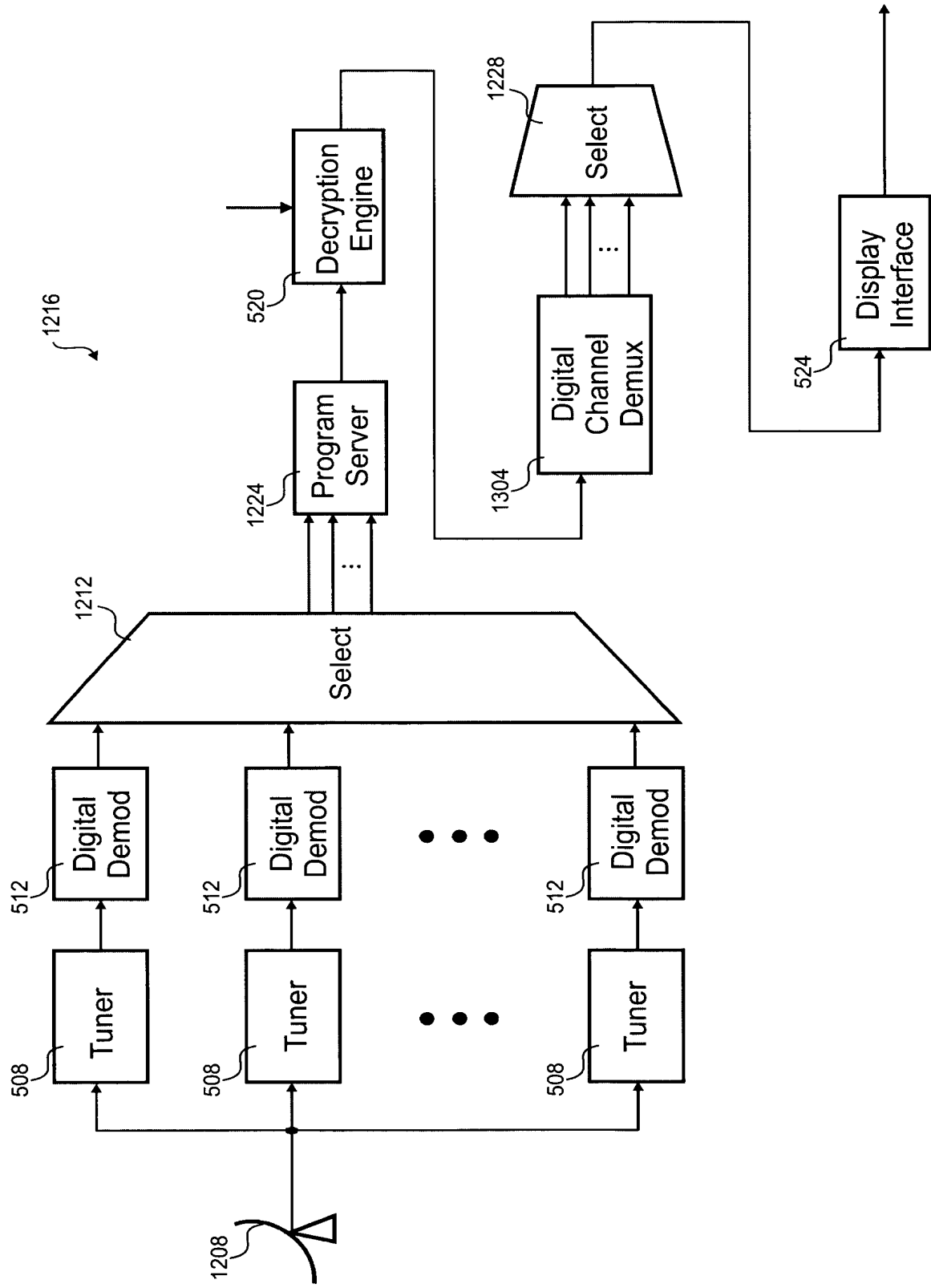


Fig. 12B

1104

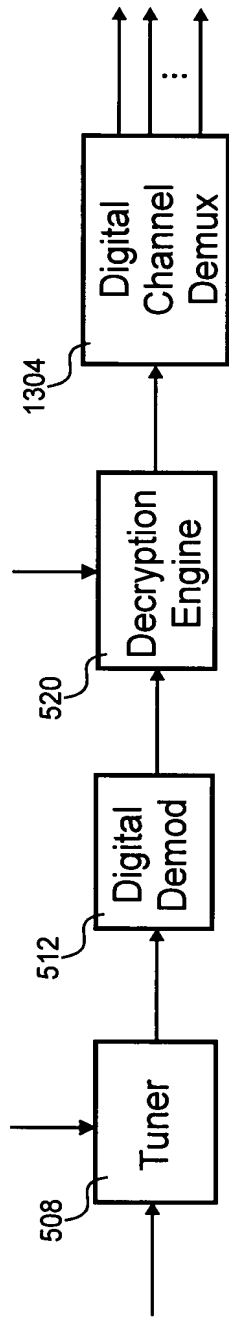


Fig. 13A

1104

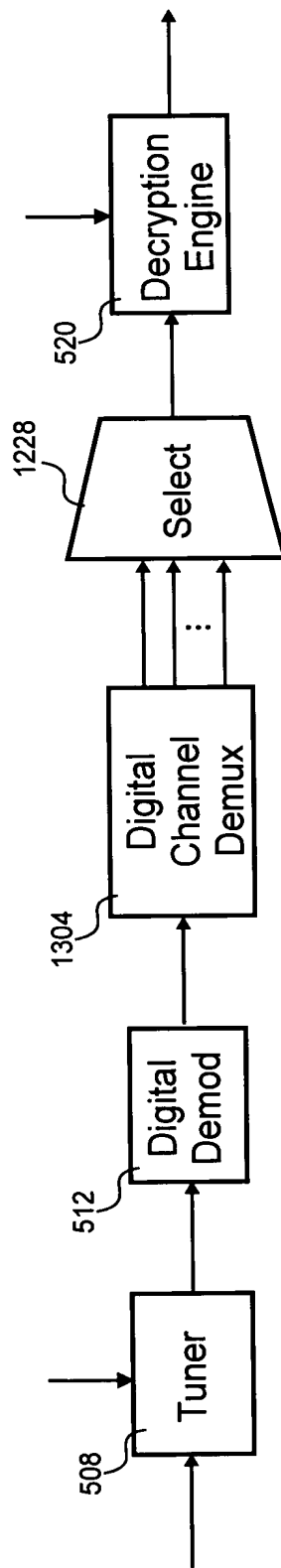


Fig. 13B

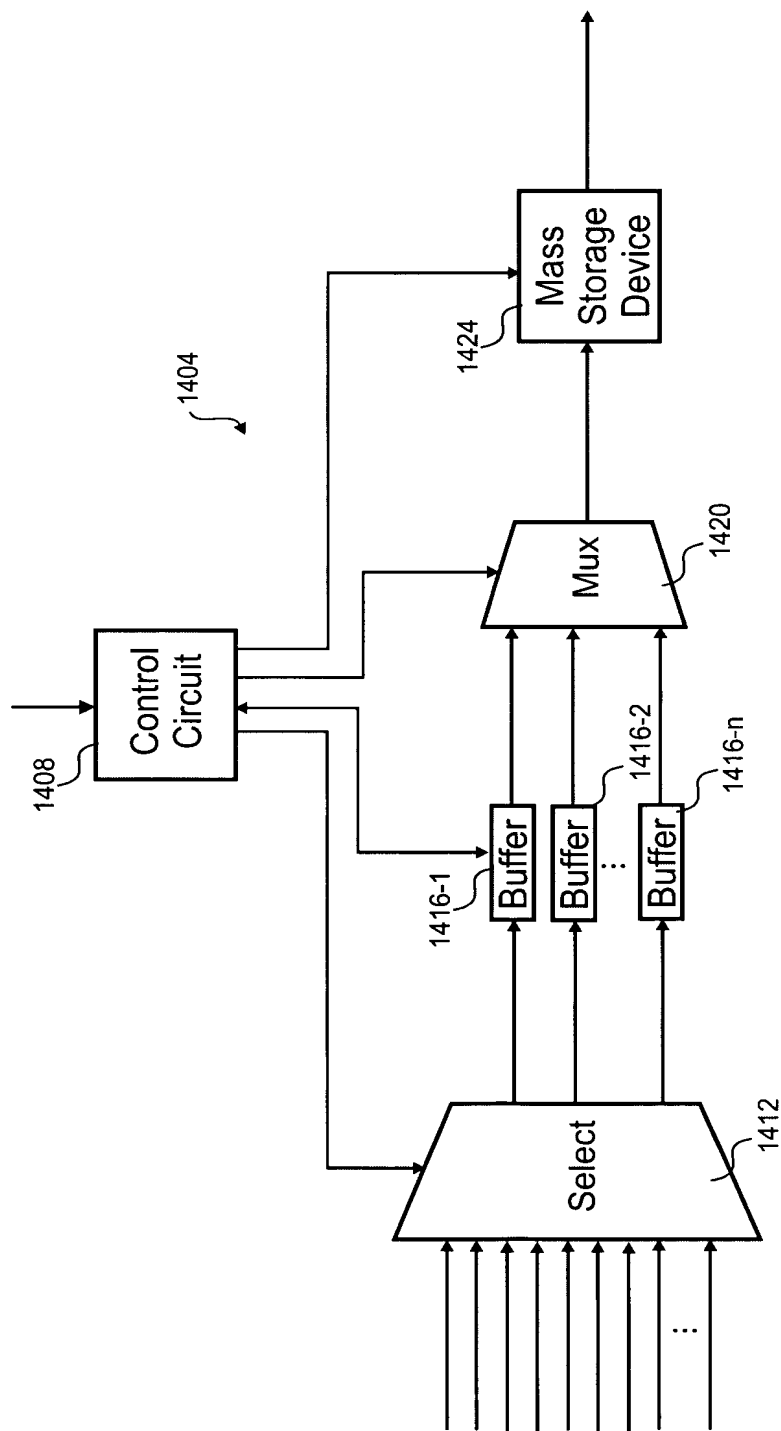


Fig. 14

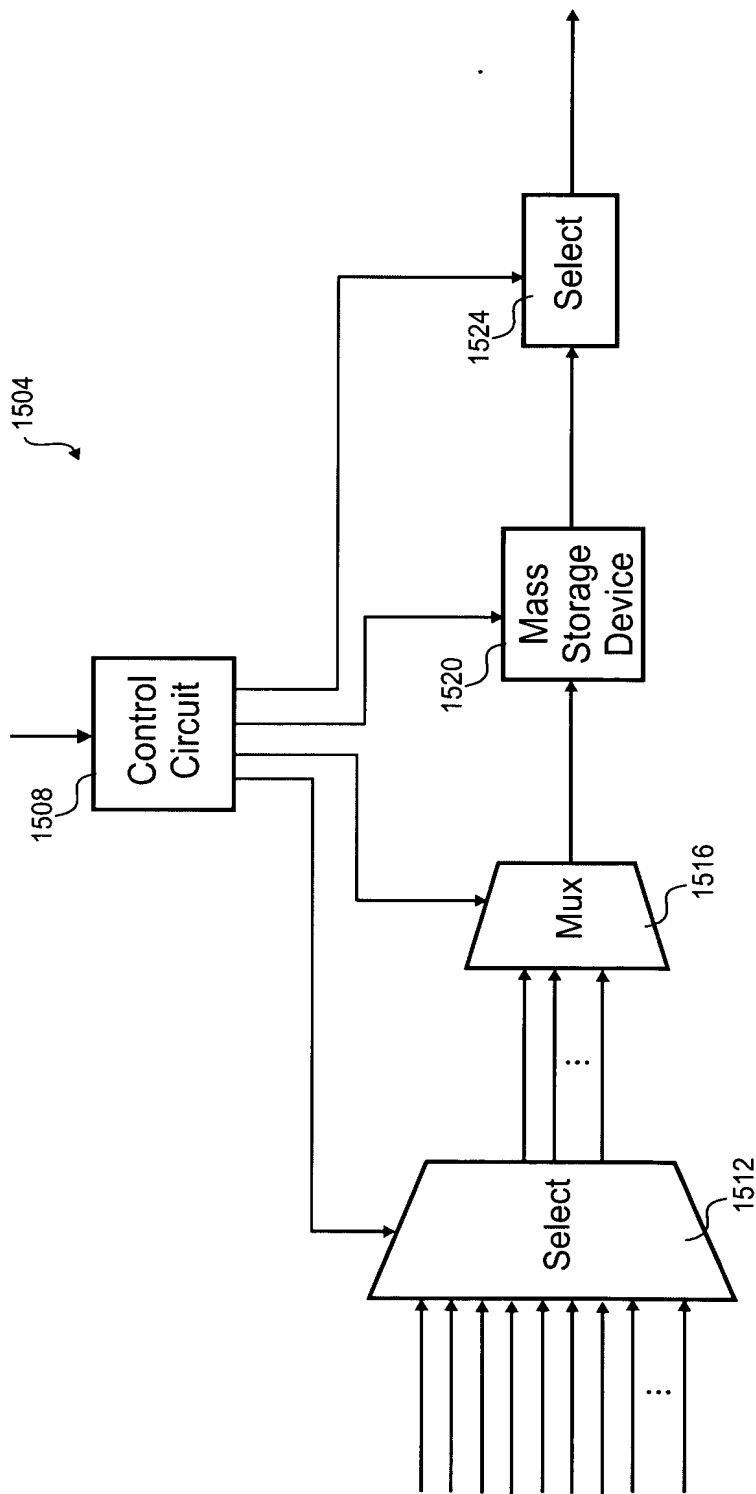


Fig. 15

FIG. 16 is a block diagram of a system 1604, including a control circuit 1608, a select circuit 1612, and a mass storage device 1616. The control circuit 1608 is connected to the select circuit 1612 and the mass storage device 1616. The select circuit 1612 is connected to the mass storage device 1616. The mass storage device 1616 is connected to an output line. The system 1604 is configured to receive input data from the select circuit 1612 and store it in the mass storage device 1616.

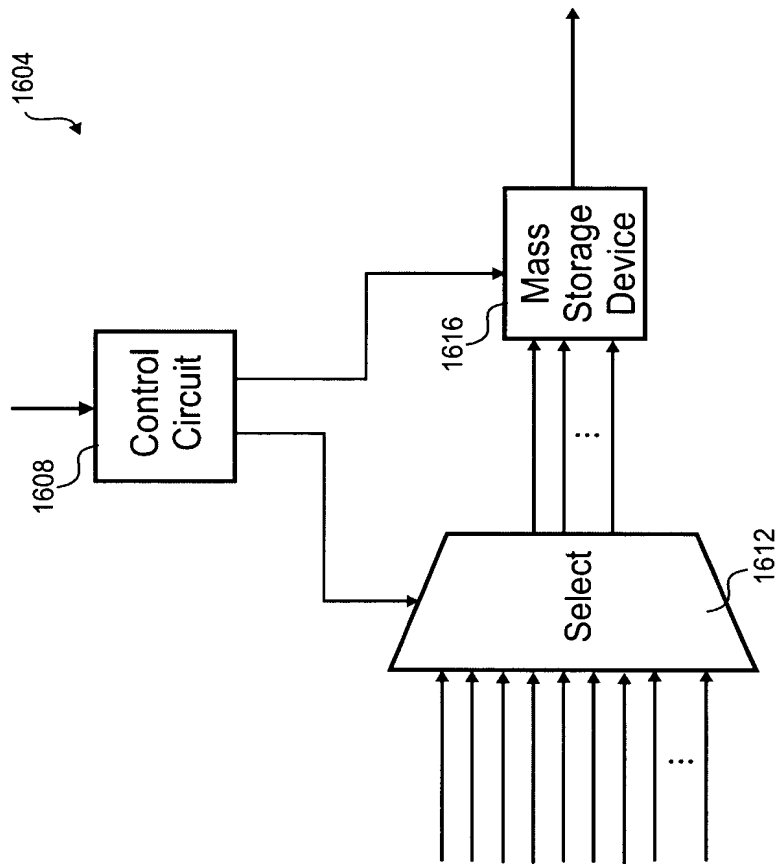


Fig. 16